IN THE SPECIFICATION:

Please rewrite the first paragraph of page 31 of the specification so as to read as follows:

"Thereafter, controller 250 compares the sum of the amount of use U calculated in step 288 and the ~~capacity~~ size D of the data of interest with the capacitance B of buffer memory 254 (290). If the sum U + D is not larger than B, buffer memory 254 has free area sufficient to register the data of interest. Controller 250 writes the data ~~capacity~~ size of the data from the first address of the non-used area buffer memory 254 (298) immediately followed by the data body 300, and sets the bit 266 corresponding to the data buffering apparatus 248 of data source field 260 of the data of interest in data information table 240 shown in Fig. 10 to "1" (320) and the process is terminated (312)."